

FIG. 1A (PRIOR ART)

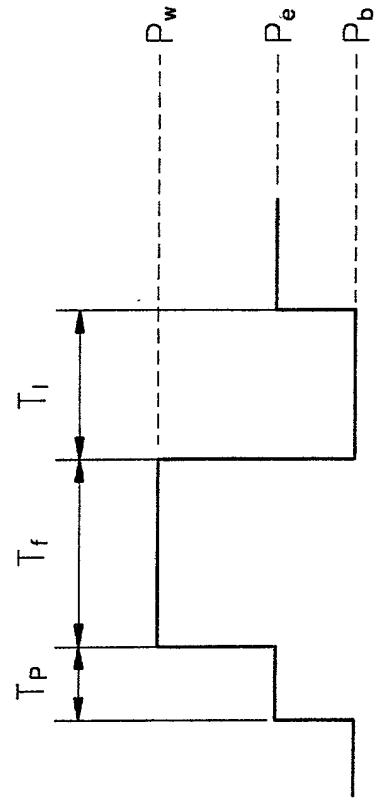


FIG. 1B (PRIOR ART)

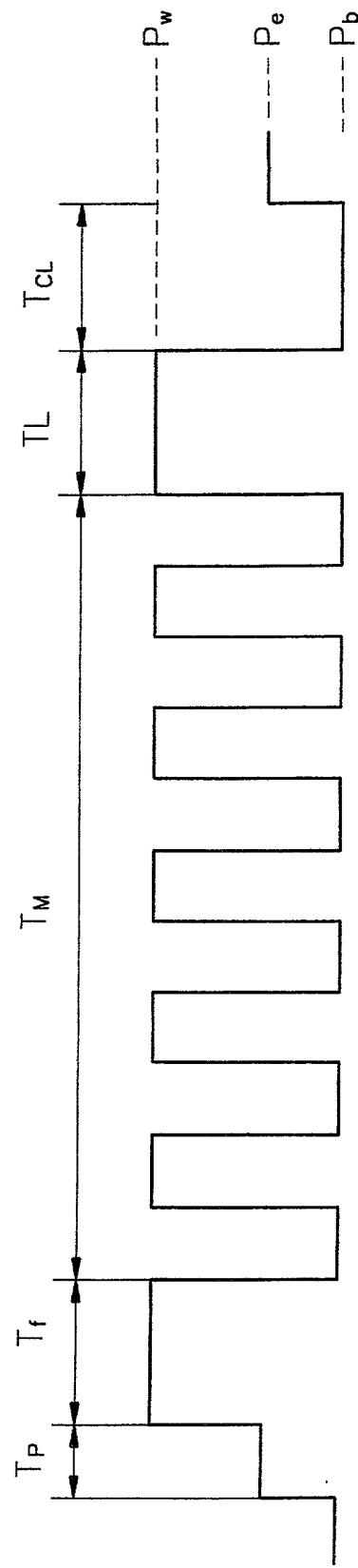


FIG. 2 (PRIOR ART)

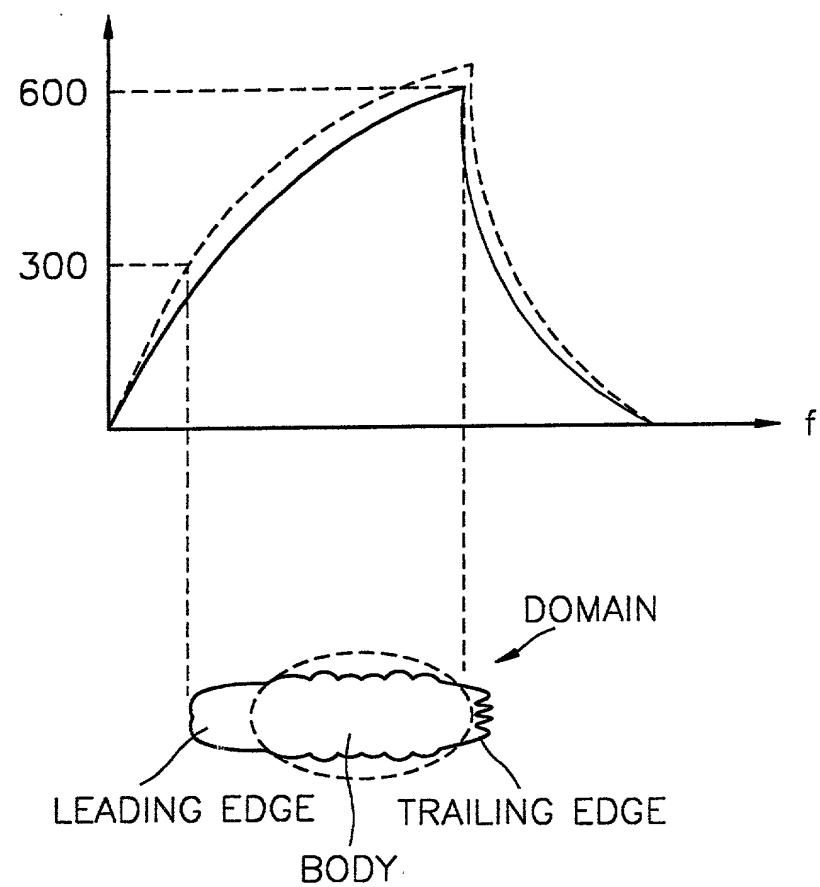


FIG. 3A

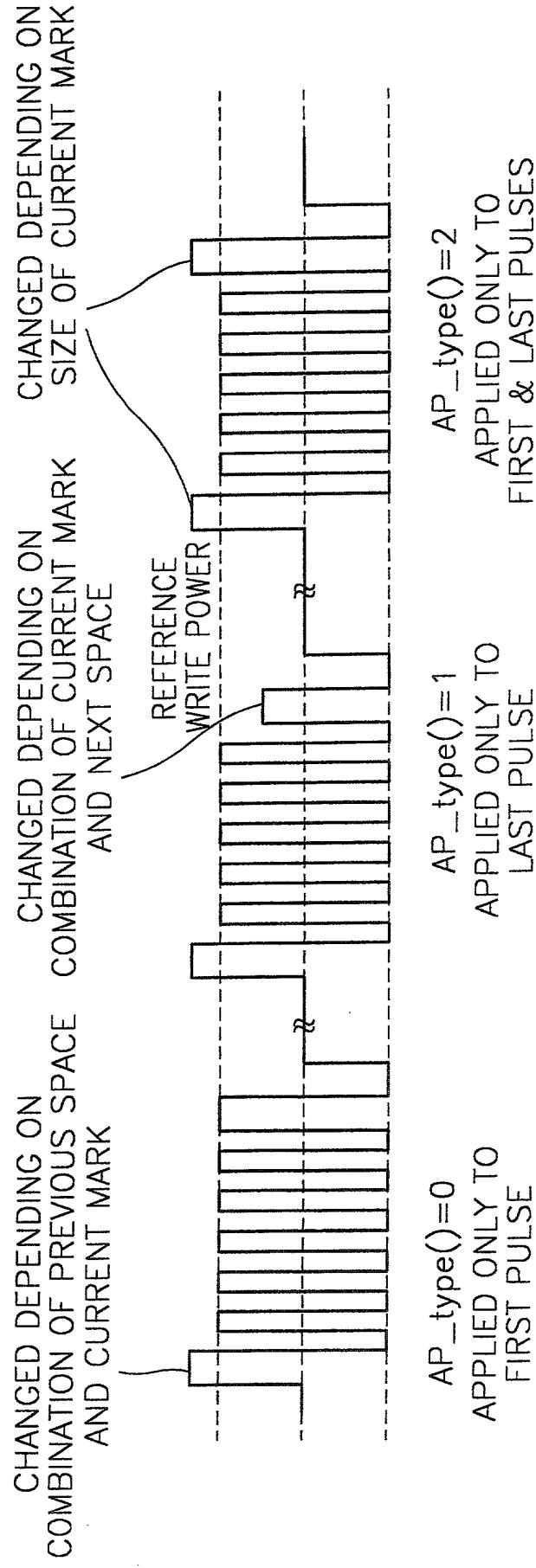
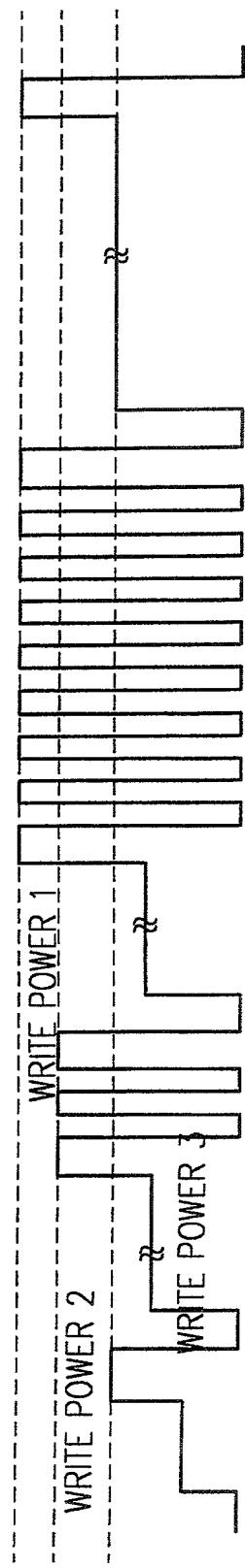


FIG. 3B



AP\_type()=3  
APPLY ADAPTIVE POWER LEVEL TO WRITE PULSE  
DEPENDING ON SIZE OF CURRENT MARK

FIG. 4

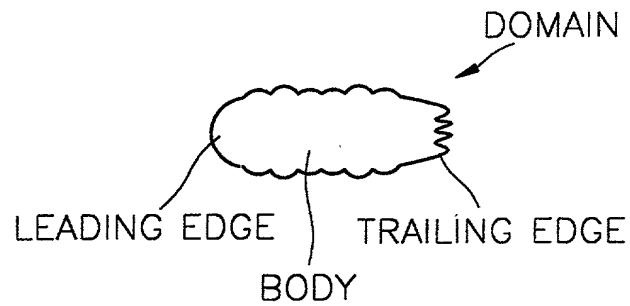


FIG. 5

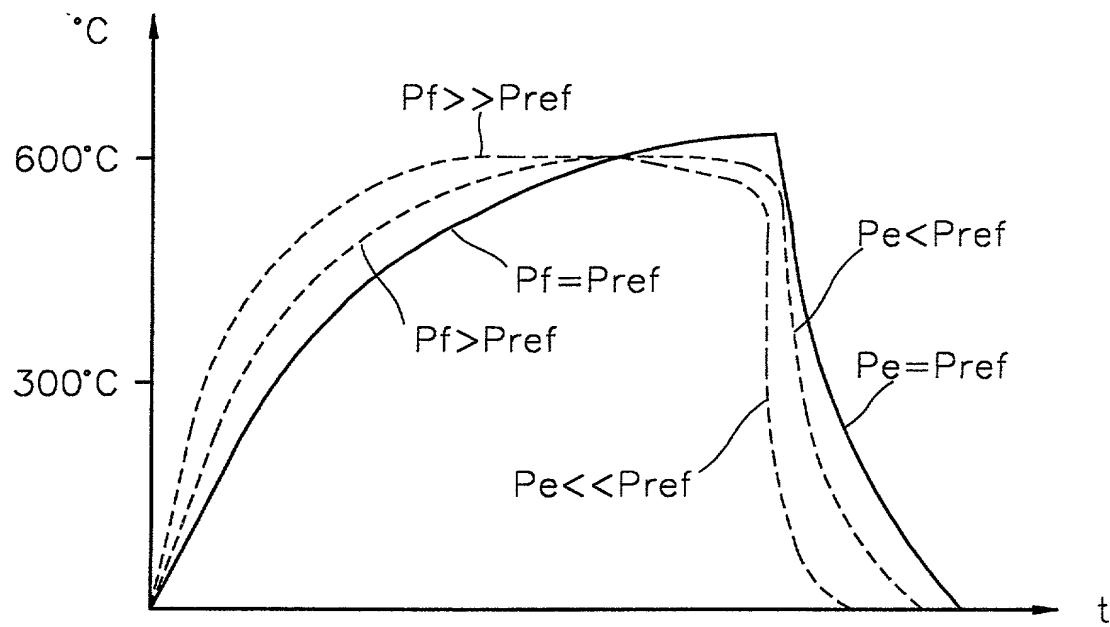


FIG. 6A

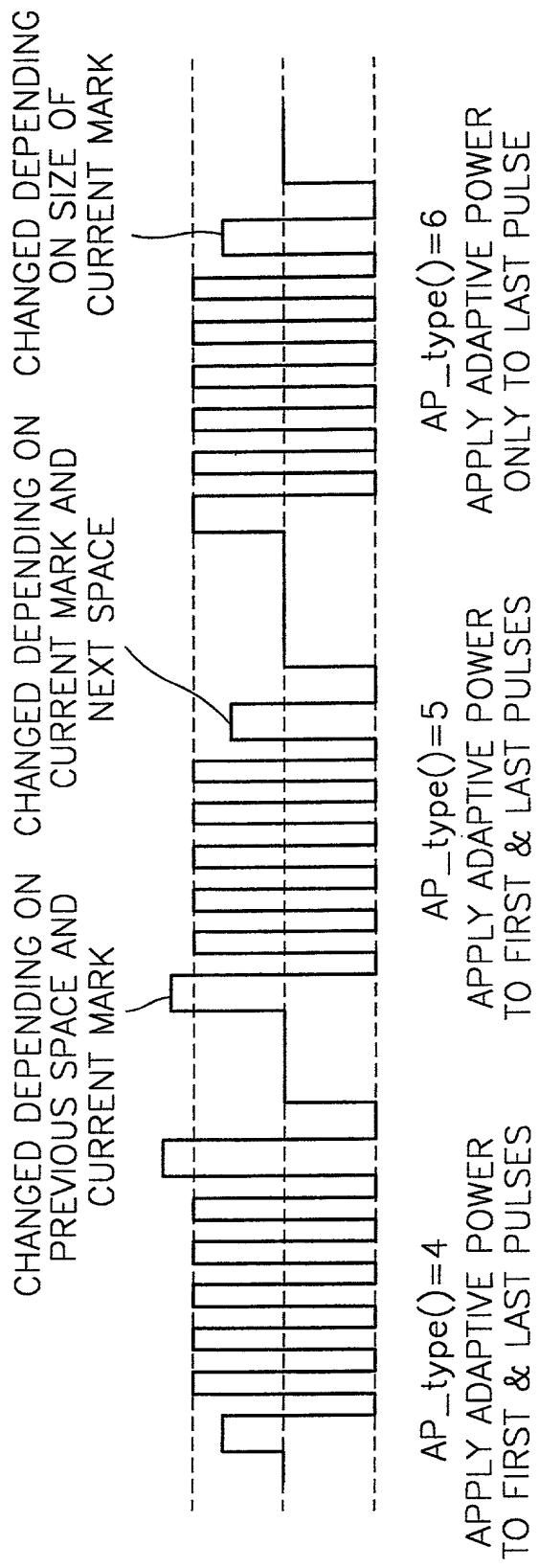


FIG. 6B

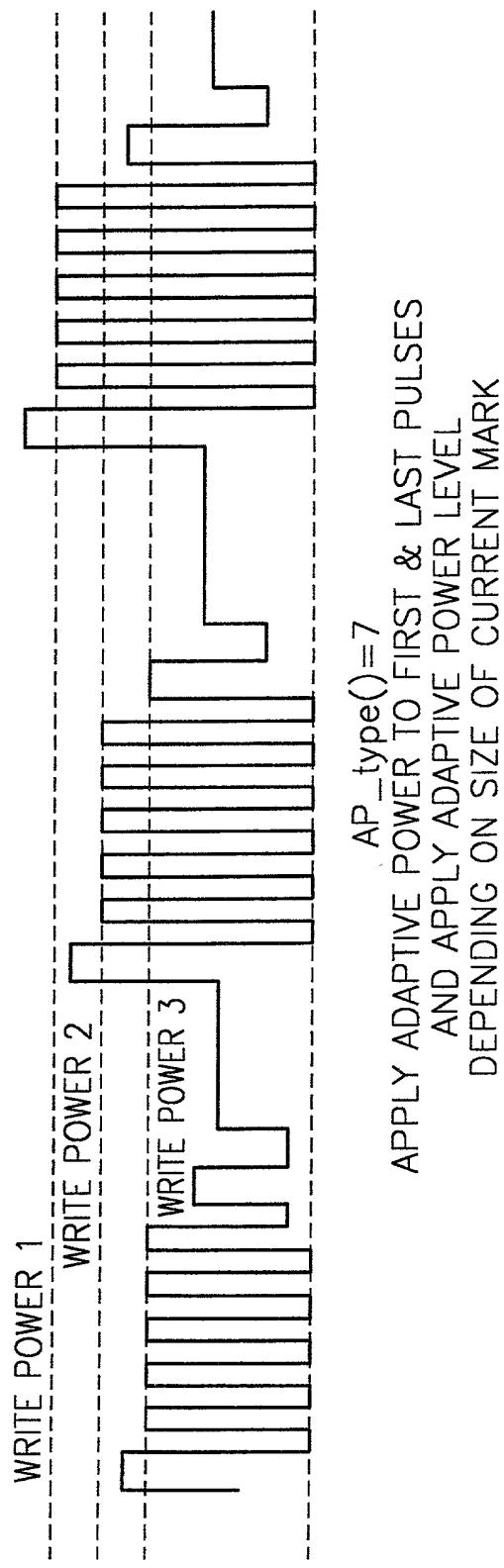
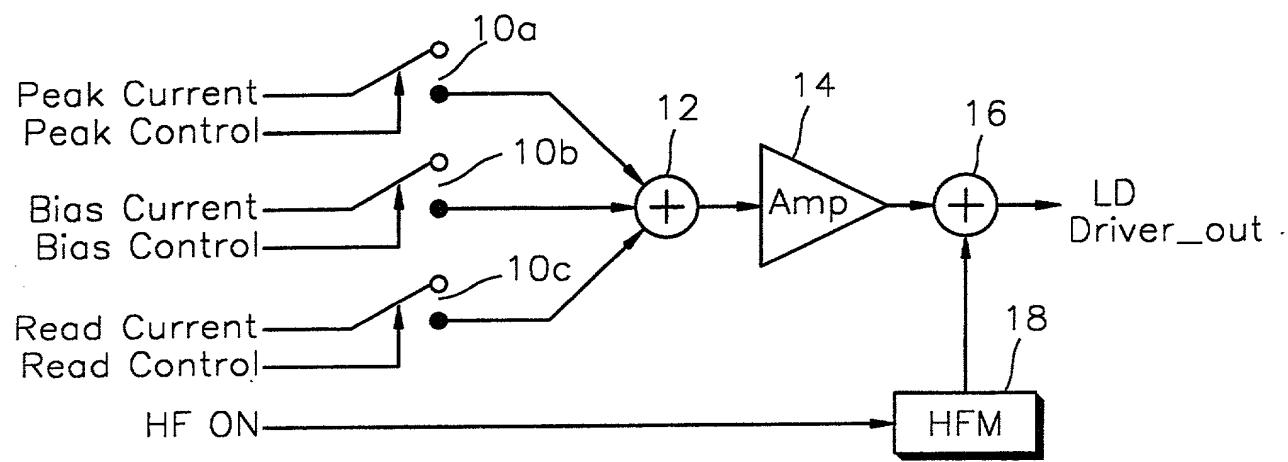


FIG. 7 (PRIOR ART)



# FIG. 8 (PRIOR ART)

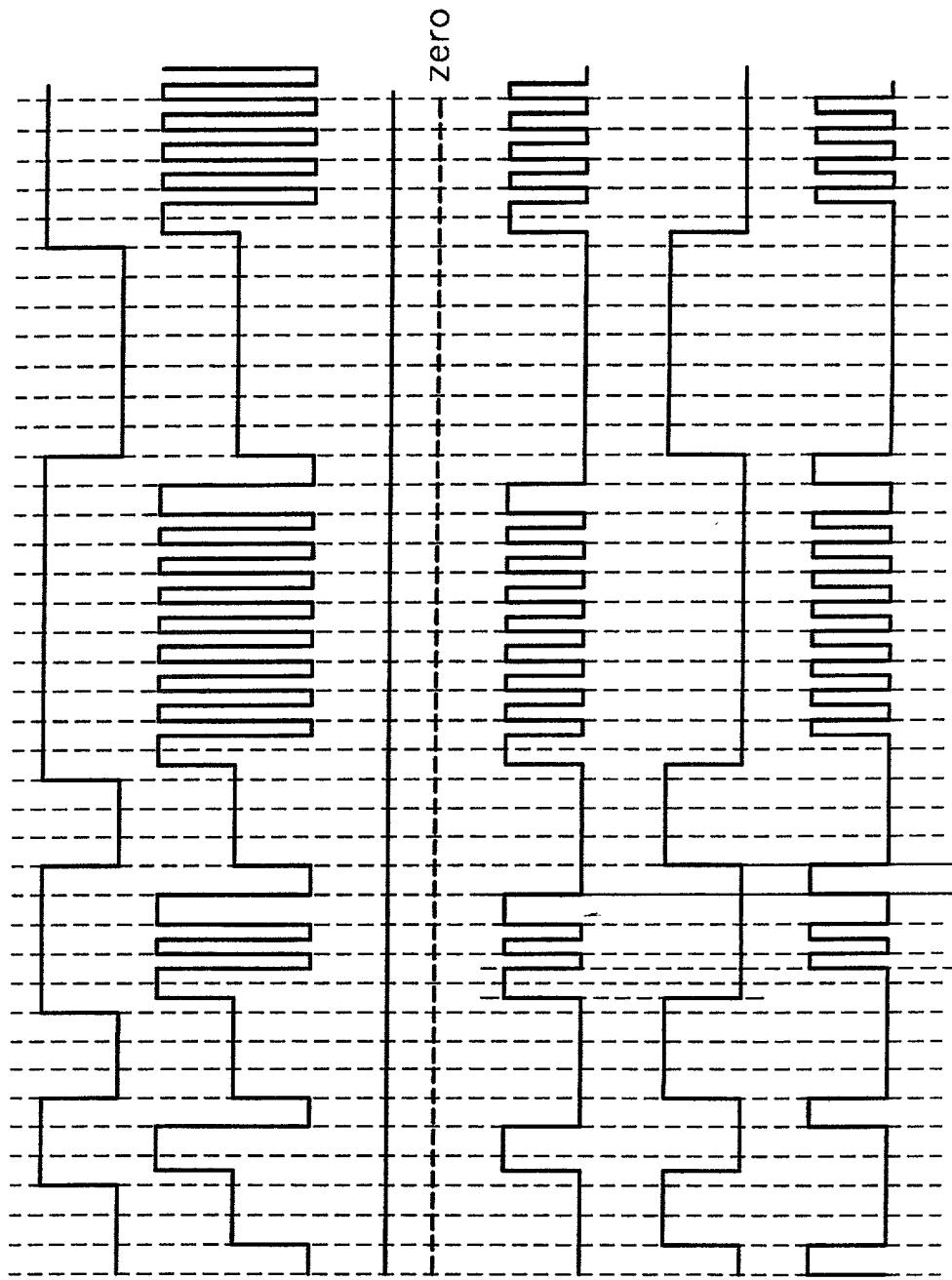


FIG. 8A INPUT NRZI DATA

FIG. 8B RECORDING PULSE  
(LD OUTPUT SIGNAL)

FIG. 8C READ Control  
(ACTIVE STATE)

FIG. 8D PEAK Control

FIG. 8E BIAS Control  
(Erase Power)

FIG. 8F Bottom Control  
(WHEN FOUR CHANNELS ARE USED)

FIG. 9 (PRIOR ART)

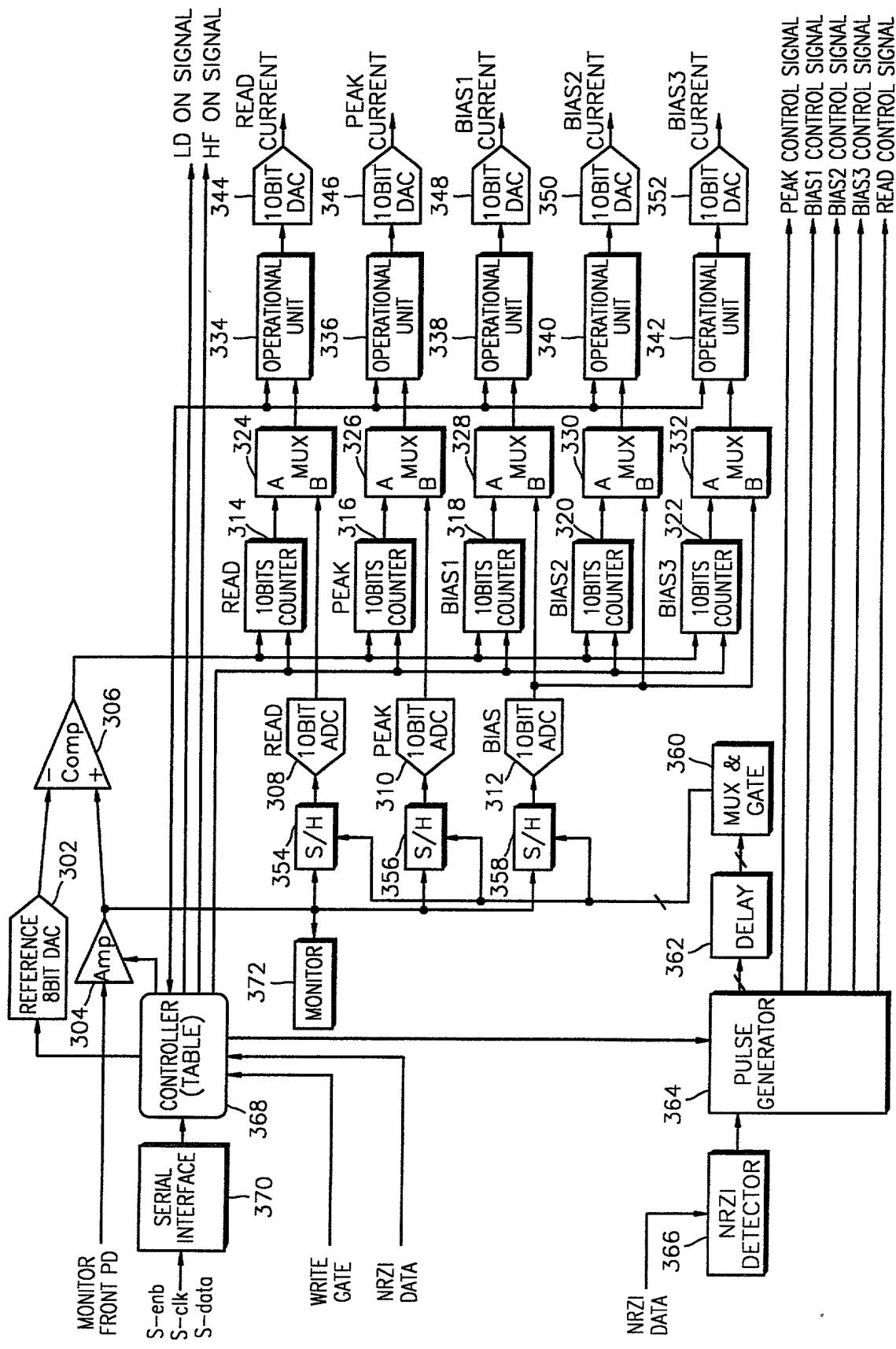


FIG. 10

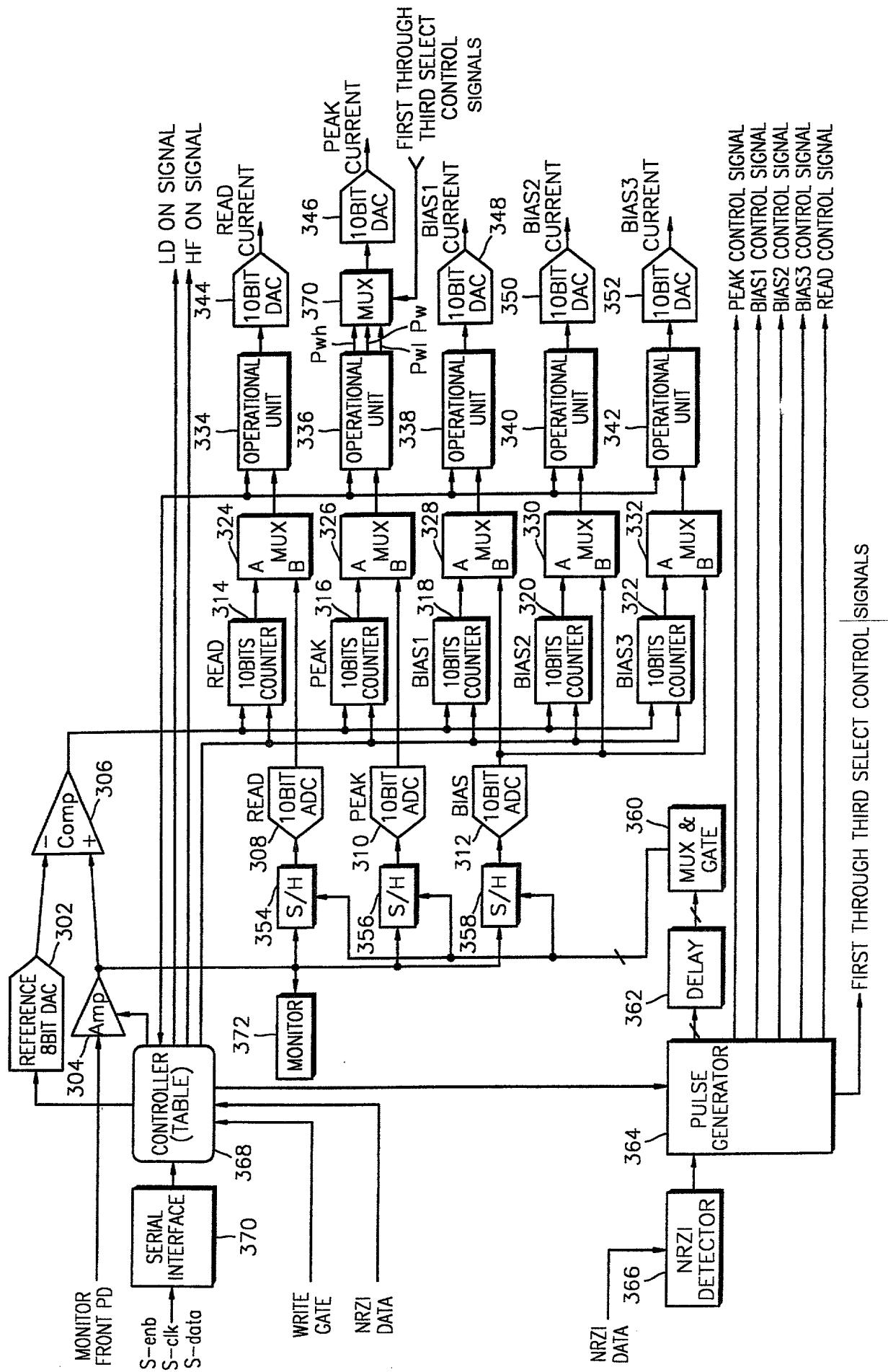


FIG. 11

